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30827 7590 03/03/2004
MCKENNA LONG & ALDRIDGE LLP
1900 K STREET, NW
WASHINGTON, DC 20006

EXAMINER

QI, ZHI QIANG

ART UNIT

PAPER NUMBER

2871

DATE MAILED: 03/03/2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/598,213	06/21/2000	Jeom Jae Kim	8733.20105	8092

TITLE OF INVENTION: MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	06/03/2004

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (OR AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WILL BE REGARDED AS ABANDONED.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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If the SMALL ENTITY is shown as NO:

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B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check the box below and enclose the PUBLICATION FEE and 1/2 the ISSUE FEE shown above.

☐ Applicant claims SMALL ENTITY status.
See 37 CFR 1.27.

II. PART B - FEE(S) TRANSMITTAL should be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). Even if the fee(s) have already been paid, Part B - Fee(s) Transmittal should be completed and returned. If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: **Mail**

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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 4 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

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30827 7590 03/03/2004

**MCKENNA LONG & ALDRIDGE LLP
1900 K STREET, NW
WASHINGTON, DC 20006**

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Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

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nonprovisional	NO	\$1330	\$0	\$1330	06/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
QI, ZHI QIANG	2871	349-129000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
- ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1	_____
2	_____
3	_____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☐ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

- ☐ Issue Fee
- ☐ Publication Fee
- ☐ Advance Order - # of Copies _____

4b. Payment of Fee(s):

- ☐ A check in the amount of the fee(s) is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☐ The Director is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

Director for Patents is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

(Authorized Signature)

(Date)

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Alexandria, Virginia 22313-1450.

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30827	7590	03/03/2004	EXAMINER	
MCKENNA LONG & ALDRIDGE LLP 1900 K STREET, NW WASHINGTON, DC 20006			QI, ZHI QIANG	
			ART UNIT	PAPER NUMBER
			2871	
DATE MAILED: 03/03/2004				

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) system (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (703) 305-1383. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at (703) 305-8283.

Notice of Allowability	Application No.	Applicant(s)	
	09/598,213	KIM ET AL.	
	Examiner	Art Unit	
	Mike Qi	2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the terminal disclaimer of Jan.21,2004.
2. ☒ The allowed claim(s) is/are 1-5,7,10-13,15,16,19,20,22-30,32-36,38,40,42 and 43.
3. ☒ The drawings filed on 21 June 2000 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: ____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date ____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date ____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date ____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date ____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other ____. |

DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Eric J. Nuss on Feb.23, 2004.

The application has been amended as follows:

Claim 15, has been changed into - - The multi-domain liquid crystal display device according to claim 12, wherein said pixel electrode overlaps said common-auxiliary electrode. - -

Allowable Subject Matter

1. Claims 1-5,7,10-13,15-16,19-20,22-30,32-36,38,40 and 42-43 are allowed.
2. The following is an examiner's statement of reasons for allowance:

The terminal disclaimer filed on Jan. 21, 2004 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of US patent No.6,335,776 has been reviewed and is accepted. The terminal disclaimer has been recorded.

The prior art of record neither discloses nor teaches a multi-domain liquid crystal display device comprising various elements as claimed, more specifically, as the following:

a storage electrode overlapping a common-auxiliary electrode in a pixel region neighboring the pixel region in the second direction [claim 1];

a storage electrode overlapping the gate bus line on the gate insulator, the storage electrode connecting the pixel electrode on the area except the pixel region, and the storage electrode overlapping the common-auxiliary electrode in a pixel region neighboring the pixel region in the second direction [claim 12].

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Art Unit: 2871


3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mike Qi whose telephone number is (571) 272-2299.

The examiner can normally be reached on M-T 8:00 am-5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mike Qi
February 23, 2004


ROBERT H. KIM
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800



PTO/SB/28 (05-03)

Approved for use through 4/30/2003. OMB 0651-0031

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**TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING
REJECTION OVER A PRIOR PATENT**

Docket Number (Optional)

8733.233.00-US

In re Application of: Jeom-Jae Kim et al.

Application No.: 09/598,213

Filed: June 21, 2000

For: MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

The owner*, LG.Philips LCD Co., LTD, of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 6,335,776. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 and 173 of the prior patent, as presently shortened by any terminal disclaimer, in the event that it later: expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR 1.321, has all claims canceled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.

Check either box 1 or 2 below, if appropriate.

1. ☐ For submissions on behalf of an organization (e.g., corporation, partnership, university, government agency, etc.), the undersigned is empowered to act on behalf of the organization.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

2. ☒ The undersigned is an attorney or agent of record.


Signature1-20-04
DateSong K. Jung
Typed or printed name(202) 496-7413
Telephone Number

- ☒ Terminal disclaimer fee under 37 CFR 1.20(d) is included.

*Statement under 37 CFR 3.73(b) is required if terminal disclaimer is signed by the assignee (owner).
Form PTO/SB/96 may be used for making this certification. See MPEP § 324.

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Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

B,
1. (currently amended) A multi-domain liquid crystal display device, comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a
plurality of data bus lines arranged in a second direction on said first substrate to define a
plurality of pixel regions, each pixel region having a common-auxiliary electrode;

a thin film transistor positioned at a crossing area of said data bus line and said gate bus
line, said thin film transistor comprising a gate electrode, a semiconductor layer, and
source/drain electrodes;

a plurality of pixel electrodes electrically charged through the thin film transistor; ~~[[and]]~~

an alignment layer on at least one substrate between said first and second substrates; and

a storage electrode overlapping a common-auxiliary electrode in a pixel region
neighboring the pixel region in the second direction.

2. (previously amended) The multi-domain liquid crystal display device according to
claim 1, wherein the common-auxiliary electrode in a first pixel region of the pixel regions has
first connecting parts which connect to the common-auxiliary electrode in a second pixel region
of the pixel regions, wherein the second pixel region neighbors the first pixel regions in the first
direction.

3. (previously amended) The multi-domain liquid crystal display device according to claim 2, wherein the first connecting parts have second connecting parts which connect to the common-auxiliary electrode in a third pixel region, wherein the third pixel region neighbors the first pixel region in the second direction.

4. (previously amended) The multi-domain liquid crystal display device according to claim 2, wherein the common-auxiliary electrode in the first pixel region has protrusions crossing the first connecting parts.

81 5. (original) The multi-domain liquid crystal display device according to claim 1, wherein said pixel electrode overlaps said common-auxiliary electrode.

6. (canceled).

7. (original) The multi-domain liquid crystal display device according to claim 1, further comprising a storage electrode overlapping said gate bus line.

8-9. (canceled).

10. (original) The multi-domain liquid crystal display device according to claim 1, wherein said pixel region is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other.

11. (original) The multi-domain liquid crystal display device according to claim 1, wherein said alignment layer is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being aligned differently from each other.

8, 12. (currently amended) A multi-domain liquid crystal display device comprising:

- first and second substrates facing each other;
- a liquid crystal layer between said first and second substrates;
- a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a plurality of pixel region;
- a common-auxiliary electrode surrounding said pixel region on a same layer whereon said gate bus line is formed;
- a gate insulator over said whole first substrate;
- a storage electrode overlapping said gate bus line on the gate insulator, the storage electrode connecting electrically the pixel electrode on an area except the pixel region and the storage electrode overlapping the common-auxiliary electrode in a pixel region neighboring the pixel region in the second direction;
- a passivation layer on said gate insulator over said whole first substrate;
- a pixel electrode electrically charged through said data bus line in said pixel region;
- a light shielding layer on said second substrate;
- a color filter layer on said light shielding layer; a common electrode on said color filter layer; and
- an alignment layer on at least one substrate between said first and second substrates.

13. (original) The multi-domain liquid crystal display device according to claim 12,
further comprising;

an n-line thin film transistor positioned at a crossing area of said data bus line and said
gate bus line

14. (canceled).

15. (original) The multi-domain liquid crystal display device according common-
auxiliary electrode.

8 | 16. (original) The multi-domain liquid crystal display device according to claim 15,
wherein said light shielding layer overlaps said common-auxiliary electrode.

17-18 (canceled).

19. (original) The multi-domain liquid crystal display device according to claim 12,
wherein said gate insulator and said passivation layer are formed in an area except said common-
auxiliary electrode.

20. (original) The multi-domain liquid crystal display device according to claim 12,
wherein said common-auxiliary electrode is electrically connected to said common electrode.

21. (canceled).

22. (original) The multi-domain liquid crystal display device according to claim 12, further comprising;
a dielectric frame for distorting electric field on said common electrode.

23. (original) The multi-domain liquid crystal display device according to claim 12, wherein said pixel electrode has a window inducing electric field therein.

24. (original) The multi-domain liquid crystal display device according to claim 12, wherein said passivation layer has a window inducing electric field therein.

81 25. (original) The multi-domain liquid crystal display device according to claim 12, wherein said gate insulator has a window inducing electric field therein.

26. (original) The multi-domain liquid crystal display device according to claim 12, wherein said common electrode has a window inducing electric field therein.

27. (original) The multi-domain liquid crystal display device according to claim 12, wherein said color filter layer has a window inducing electric field therein.

28. (original) The multi-domain liquid crystal display device according to claim 12, further comprising;

an over coat layer on said color filter layer.

29. (original) The multi-domain liquid crystal display device according to claim 28, wherein said over coat layer has a window inducing electric field therein.

30. (original) The multi-domain liquid crystal display device according to claim 12, wherein said passivation layer includes a material selected from the group consisting of BCB (BenzoCycloButene), acrylic resin, and polyimide compound.

31. (canceled).

8, 32. (original) The multi-domain liquid crystal display device according to claim 12, wherein said common-auxiliary electrode includes a material selected from the group consisting of ITO (indium tin oxide), aluminum, molybdenum, chromium, tantalum, titanium, and an alloy thereof.

33. (original) The multi-domain liquid crystal display device according to claim 12, wherein said pixel electrode includes a material selected from the group consisting of ISO (indium tin oxide), aluminum, and chromium.

34. (original) The multi-domain liquid crystal display device according to claim 12, wherein said common electrode includes ITO (indium tin oxide).

35. (original) The multi-domain liquid crystal display device according to claim 12, wherein said pixel region is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other.

36. (original) The multi-domain liquid crystal display device according to claim 12, wherein said alignment layer is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being aligned differently from each other.

37. (canceled).

38. (original) The multi-domain liquid crystal display device according to claim 36, wherein all portions of said at least two portions of the alignment layer are non alignment-treated.

39. (canceled).

40. (original) The multi-domain liquid crystal display device according to claim 12, wherein said liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy.

41. (canceled).

42. (original) The multi-domain liquid crystal di-play device according to claim 12,
further comprising:

a negative biaxial film on at least one substrate.

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43. (original) The multi-domain liquid crystal display device according to claim 12,
wherein said liquid crystal layer includes chiral dopants.
